

PATENT
8008-1052

IN THE U.S. PATENT AND TRADEMARK OFFICE

In re application of

Yasutaka NAKASHIBA

Conf. 2273

Application No. 10/812,282

Group 2815

Filed March 30, 2004

Examiner J. Jackson, Jr.

SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

REQUEST FOR RECONSIDERATION

Assistant Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

May 4, 2009

Sir:

In response to the Official Action of January 2, 2009, for which the period for response has been extended one month, reconsideration of the rejection of the claims is respectfully requested.

Remarks begin on page 2 of this paper.